



# Academic Affairs - Course Proposal Form

CALIFORNIA STATE UNIVERSITY, SACRAMENTO

Academic Unit: <b>EEE</b>		Department Chair: <b>Dr. S.K. Ramesh</b>	
Type of Course Proposal:  New <input checked="" type="checkbox"/> Change <input type="checkbox"/> Deletion <input type="checkbox"/>		Date: <b>February 23, 2006</b>	
Does this course fulfill a requirement for single-subject or multiple subject credential students? Yes <input type="checkbox"/> No <input checked="" type="checkbox"/>		For Catalog Copy: Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>	CCE: Yes <input type="checkbox"/> No <input checked="" type="checkbox"/>
		Semester Effective: Fall <input checked="" type="checkbox"/> Spring <input type="checkbox"/> 20_06__	
Prefix & No. <b>EEE 286</b>	Title: <b>Microcomputer System Design II</b>		Units: <b>3</b>

Change to:

Prefix & No.	Title:	Units:

### JUSTIFICATION:

Topics are updated due to technological changes in the computer industry.

### NEW COURSE DESCRIPTION: (Not to exceed 80 words, and language should conform to catalog copy.)

See <http://www.csus.edu/acaf/univmanual/crspsl.htm> - Guidelines for Catalog Course Description

Includes PCI and PCI express bus specifications/architecture, PCI bridges transaction ordering, PCI express transactions and handshaking protocols, electromagnetic interference, methods of eliminating interference, shielding grounding, balancing, filtering, isolation, separation, orientation, cancellation techniques and cable design. Class involves design projects and research presentations on PCI and PCI Express Bridge.

Note:

Prerequisite: **EEE 285 or CpE 186**

Corequisite:

CAN (California Articulation Number):

Graded: Letter  Credit/NoCredit  Instructor Approval? Yes  No

Course Classification: Title for SIS+ (not more than 25 characters)  
**Microcomputer Sys Des II**

Cross Listed? Yes  No  If yes, with what course:

How Many Times Can This Course be Taken for Credit?  
**Once**

