# Course Change Proposal
**Form A**

<table>
<thead>
<tr>
<th>Academic Group (College):</th>
<th>Academic Organization (Department):</th>
<th>Date:</th>
<th>09/18/2006</th>
</tr>
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<tbody>
<tr>
<td>ECS</td>
<td>EEE</td>
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**Type of Course Proposal:**
- New ___
- Change _X_
- Deletion ___

**Department Chair:**
Dr. Suresh Vadhva

**Submitted by:**
Dr. Suresh Vadhva

**Does this course fulfill a requirement for single-subject or multiple subject credential students?**
- Yes ___
- No _X_

**For Catalog Copy:**
- Yes _X_
- No ___

**CCE:**
- Yes ___
- No _X_

**Semester Effective:**
- Fall ___
- Spring _X_, 20_07__

This course replaces experimental course **Subject Area (prefix) and Catalog Number (course number):**

EEE 296K

This Catalog Number (course number) is being replaced:

<table>
<thead>
<tr>
<th>Subject Area (prefix) &amp; Catalog No. (course no.):</th>
<th>Title:</th>
<th>Units:</th>
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<tbody>
<tr>
<td>EEE 296K</td>
<td>Advanced VLSI Design</td>
<td>3</td>
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**Change to:**

<table>
<thead>
<tr>
<th>Subject Area (prefix) &amp; Catalog No. (course no.):</th>
<th>Title:</th>
<th>Units:</th>
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<tbody>
<tr>
<td>EEE 239</td>
<td>Advanced VLSI Design-For-Test II</td>
<td>3</td>
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**JUSTIFICATION:**
To build a greater knowledge depth among students of CMOS logic and VLSI fabrication, assembly and test operations. These are items that would make our students more competitive in the electronics industry.

**NEW COURSE DESCRIPTION:**
(Not to exceed 80 words, and language should conform to catalog copy. See http://www.csus.edu/acaf/univmanual/crspsl.htm - Guidelines for Catalog Course Description)

Advanced topics in VLSI testing and Design-For-Test applications. Memory-specific test methodology and special features of memory designs employed in high volume manufacturing for improved testability, yield, and reliability. VLSI failure modes, their detection and prevention. Application of trim, redundancy, wear-leveling and error correction.

**Note:**
Prerequisite: EEE 238

**Corequisite:**

**CAN (California Articulation Number):**

<table>
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<tr>
<th>Graded:</th>
<th>Letter</th>
<th>Credit/No Credit</th>
<th>Instructor Approval Required</th>
<th>Yes ___</th>
<th>No <em>X</em></th>
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<tbody>
<tr>
<td>C5 Lecture</td>
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**Course Classification (e.g., lecture, lab, seminar, discussion):**
Title for SIS+/CMS (not more than 30 characters)
Adv VLSI Design-For-Test II

**Cross Listed?**
- Yes ___
- No _X_

If yes, do they meet together and fulfill the same requirement, and what is the other course.

**How Many Times Can This Course be Taken for Credit?**
- Once

**Can the course be taken for Credit more than once during the same term?**
- Yes ___
- No _X_
FOR NEW COURSE PROPOSALS OR SUBSTANTIVE CHANGES ONLY:

**Description of the Expected Learning Outcomes:** Describe outcomes using the following format: “Students will be able to: 1), 2), etc.” See the example at http://www.csus.edu/acaf/example.htm

Students will become familiar with the advanced issues involved with taking a finished integrated circuit design and developing it into a validated, mass production-worthy product. Students will learn the problems encountered in the development of integrated circuits and the methods used to solve them.

**Attach a list of the required/recommended course readings and activities [Note: it is understood that these are updated and modified as needed by the instructor(s).] This attachment should be forwarded only to your Dean's office, not Academic Affairs.**

**Assessment Strategies:** A description of the assessment strategies (e.g., portfolios, examinations, performances, pre-and post-tests, conferences with students, student papers) which will be used by the instructor to determine the extent to which students have achieved the learning outcomes noted above:

**Evaluations will be based on the following:**

- 2 midterm exams and 1 final exam
- Research term paper

**For whom is this course being developed?**

<table>
<thead>
<tr>
<th>Majors in the Dept</th>
<th>Majors of other Depts</th>
<th>Minors in the Dept</th>
<th>General Education</th>
<th>Other</th>
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**Is this course required in a degree program (major, minor, graduate degree, certificate)?**

- Yes __
- No ___

If yes, identify program(s):

**Does the proposed change or addition cause a significant increase in the use of College or University resources (lab room, computer facilities, faculty, etc.)?**

- Yes ___
- No ___

If yes, attach a description of resources needed and verify that resources are available.

Indicate which department or programs will be affected by the proposed course (if any).

*The Department Chair's signature below indicates that affected programs have been sent a copy of this proposal form.*

**Approvals:** If proposed change, new course or deletion is approved, sign and date below. If not approved, forward without signing to the next reviewing authority, and attach an explanatory memorandum to the original copy.

**Signatures:**

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<thead>
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<th>Department Chair</th>
<th>Date</th>
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<th>College Dean or Associate Dean</th>
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<tr>
<th>CPSP (for school personnel courses ONLY)</th>
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<th>Associate Vice President and Dean for Academic Programs</th>
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**Distribution: Academic Affairs (original), Department Chair and College Dean. Dean’s office to send original after approval to Academic Affairs, at mail zip 6016. An electronic copy must also be sent.**