Department of Electrical and Electronic Engineering, California State University, Sacramento

## Engr 17 Introductory Circuit Analysis, graded, 3 units

Instructor: Tatro - Spring 2019 Section 2, Call No. 32401, Mon/Wed 09:00 AM – 09:50 PM, Kadema Hall, Room 145 Fri Web Online Section 4, Call No. 32571, Mon/Wed 10:00 PM – 10:50 PM, Kadema Hall, Room 145 Fri Web Online

**Course Content:** Writing of mesh and node equations. DC and transient circuit analysis by linear differential equation techniques. Application of laws and theorems of Kirchhoff, Ohm, Thévenin, Norton and maximum power transfer. Sinusoidal analysis using phasors, average power.

Prerequisite: Phys 11C and Math 45; either the math or physics may be taken concurrently, but not both.

Textbook:	<u>Electric Circuits</u> , Nilsson and Riedel, 10 <sup>th</sup> Edition, 2014, Prentice Hall, ISBN: 978-0133760033				
Instructor:	Russ Tatro email: <u>rtatro@csus.edu</u> Office Phone: 278-4878 Office Hours: See my websit		Office: Riverside 5030 Website: <u>www.csus.edu/indiv/t/tatror</u> for current office hours.		
Grading:	Midterm I Midterm II Final Exam Homework Quizzes	15% 15% 20% 30% 20%			

**Course Goals**: Introduce the fundamental tools of linear circuit analysis which is useful to all engineers. Develop the fundamentals of circuits, including wires, resistors, capacitors, inductors, voltage and current sources, and operational amplifiers. Prepare students for more advanced courses in electronic applications and circuit analysis.

**Hybrid Course**: This course will be offered in a <u>hybrid</u> format with both in classroom sessions and online events. We will meet in the classroom on Mondays and Wednesdays. The Friday class time will be dedicated to online materials at your own pace. The online material may include pre-recorded videos, online quizzes, outside class reading assignments and other online sessions. All course materials will be available on ECS Moodle: <u>https://moodle2.ecs.csus.edu/</u>. See the last page of this syllabus on how to self-enroll into the Moodle based Engr 17 course.

We tell ABET that after this course the student will be able to:

- 1. Identify linear systems and represent those systems in schematic form.
- 2. Apply Ohm's Law and Kirchhoff's current/voltage laws to circuit problems.
- 3. Simplify circuits using series and parallel equivalents. Simplify circuits to their Thévenin and Norton equivalents.
- 4. Perform node and loop analyses and state the system of linear equations in standard matrix form.
- 5. Apply the concepts of energy and power to solving circuit problems.
- 6. Identify and model first order electric systems involving capacitors and inductors.
- 7. Perform circuit analysis by the steady-state phasor method of time-varying signals (phasors).

**Homework:** Homework assignments will be completed online using ECS Moodle activities in the course Moodle site. There is homework every week and each chapter will have at least one homework assignment. Problems shall either be from the textbook or created by the instructor. Most assignments are released Monday mornings at 6 am. Most homework assignments are due on Monday mornings at 5:00 am. All homework material is testable whether covered in class or only in the homework assignment. There is no time limit on completing the homework assignment as long as you complete the homework by the due date. You can "submit" the homework as many times as you wish until you get a perfect 100% on the assignment. You receive the exact same questions on every attempt on the homework so there is no reason not to attempt a less-than perfect homework score again and again.

**Quizzes:** There will be a 60 minute quiz each week (except for exam weeks). The quizzes are self-paced online between the hours of 6 am and midnight each Wednesday in the ECS Moodle Quiz activity. The quiz must be completed in one session (no starting nor stopping with a break) in timed one continuous hour. The quizzes are "once and done" with only one submission allowed.

**Exams:** There will be two 60 minute midterm exams and a two hour final exam during the semester. The exams are a timed test completed online using an ECS Moodle Quiz module as scheduled in the syllabus. The student will use the online access of their choice and should make appropriate arrangements to take the exam online during the scheduled day/time. The exams are "once and done" with only one submission allowed.

**Prior written permission** is required for all make-up exams and then only with compelling reasons in accordance with and as outlined by University policy.

**Grading Policy:** The course will be graded in accordance with University guidelines using the "+" and "-" method as called for by the University. Grades may be curved at the instructor's discretion. The class average is usually in the C+ range. Typical (meaning somewhere around this region) grades ranges are: "A" 94.5 and above "A-" 89.5 to 94.49 "B+" 87.5 to 89.49 "B" 83.5 to 87.49 "B-" 79.5 to 83.49 "C+" 77.5 to 79.49 "C" 73.5 to 77.49 "C-" 69.5 to 73.49 "D+" 67.5 to 69.49 "D" 63.5 to 67.49 "D-" 59.5 to 63.49 F Below 59.5

## Why take this course?

**CpE and EEE** - This material is fundamental to your understanding of upper division required courses. You must have an internal intuition as to the effects of resistance, capacitance and inductance whether explicit in the circuit or implicit based on circuit element proximity.

**ME** – Most mechanical systems include controls, electronic systems and move various amounts of power. Increasingly, this takes the form of software control by electronic systems, movement by electric motors, and other circuit related techniques. Your FE and PE exams include questions covered by this course and perhaps only by this course.

## **Instructional Strategy?**

First acquire circuit analysis skills with only DC sources. This is the simplest possible circumstance. This is chapter 1 through chapter 5.

Then explore devices (capacitors and inductors) that react usefully to time varying sources. This is chapter 6.

Then bring it all together to analyze circuits when the source may be DC, time varying or both. The phasor form is extremely useful and covered in chapter 9.

Finally chapter 10 gives you the skills to examine large power transfer basics so needed in utilities, the renewable energy evolution and fundamentals of energy storage (think electric vehicles).

## **Course Demands?**

Most students will find the pace challenging. You should plan on at least 10 hours a week on just this course. The time demand is absolutely worth it. This is fundamental career knowledge and needs your motivation and intense focus. Across the engineering disciplines – you need this. Not convinced? Go find a senior doing their senior project or a new engineer in the field. See what they say.

Week Date Chapter Topics: 1-21 1 Martin Luther King, Jr. Holiday – Campus Closed 1-23 1.1 - 1.4Course Introduction – SI Units, Voltage and Current Passive Sign Convention, Power and Energy 1-25 1.5 - 1.62 Circuit Elements – R, L and C, Electrical Resistance 1-28 2.1 - 2.21-30 2.4 - 2.5Kirchhoff's Laws – KCL and KVL, Circuits with Dependent 2-01 Sources 3 2-04 3.1 - 3.2Resistors in Series and Parallel 2-06 3.3 - 3.4Voltage and Current Division 2-08 4 2-11 Handout Using Matlab to solve linear equations 2-13 4.1 - 4.2Nodal Analysis, 2-15 5 2-18 4.3 - 4.4Node analysis with dependent sources, Special Cases Chapters 1, 2, 3 - Sec 2 09:00 pm to 10:00 pm 2-20 Exam 1 2-22 Chapters 1, 2, 3 - Sec 4 10:00 am to 11:00 am 2-25 4.5 Mesh Analysis 6 2-27 4.6 - 4.7Mesh analysis with dependent sources, Special Cases 3-01 7 Equivalent Circuits - Source Transformations 3-04 4.9 3-06 4.10 Thévenin and Norton Equivalent Circuits 4.11 - 4.12 Test Voltage/Current Method and Max Power Transfer 3-08 8 The Ideal Operational Amplifier 3-11 5.1 3-13 5.2 - 5.6Analyzing the OpAmp 3-15 9 3-18 3-20 Spring Recess 3-22 10 3-25 6.1 - 6.2Inductor & Capacitor 3-27 Series/Parallel Inductors and Capacitors 6.3 3-29 11 4-01 Cesar Chavez Birthday Observed – Campus Closed Chapters 4, 5 and 6 (partial) - Sec 2 09:00 pm to 10:00 pm 4-03 Exam 2 4-05 Chapters 4, 5 and 6 (partial) - Sec 4 10:00 am to 11:00 am 9.1 - 9.3Sinusoidal Source, the Sinusoidal Response, the Phasor 12 4-08 4-10 The Phasor and Passive Circuit Elements 9.4 4-12 13 4-15 9.5, 9.7 - 9.9Analysis of Circuit Elements in the Frequency Domain 4-17 6.4 Mutual Inductance 4-19 14 4-22 9.10 The Transformer 9.11 4-24 The Ideal Transformer and Impedance Matching 4-26 10.1 - 10.215 4-29 Instantaneous and Average Power 5-01 10.3 rms Value 5-03 5-06 16 End of course wrap-up 5-08 5-10 17 **Final Exam** Final exam covers Chapters 6 (Mutual Inductance), 9 and 10 Section 2 - Wed 5/15/2019 8:00 am – 10:00 am Online Exam Section 4 – Tue 5/14/2019 8:00 am – 10:00 am Online Exam

Engr 17 - Course Outline - Spring 2019

Engr 17 – Quiz, homework, and video assignments Due Dates below may shift slightly – see the online schedule for latest deadlines

Week	Date	Online Quiz	Homework	Videos
1	1-21	<b>S</b>		Chapter 1
	1-23			•
	1-25			
2	1-28		H01 - Chapter 1	Chapter 2
	1-30	Q1 - Chapter 1	_	_
	2-01			
3	2-04		H02 - Chapter 2	
	2-06	Q2 - Chapter 2		
	2-08			Chapter 3
4	2-11		H03 - Chapter 2 and 3	
	2-13	Q3 - Chapter 3		
	2-15			
5	2-18		H04 - Chapter 3 and Matlab	Chapter 4
	2-20	Exam 1		
-	2-22			
6	2-25	O4 Chantas 4	H05 – Chapter 4	
	2-27	Q4 – Chapter 4		
7	3-01		U06 Chantar 4	
/	3-04	05 Using Matlah	Huo - Chapter 4	
	3.08	Q5 - Osing Matlab		Chapter 5
8	3-11		H07 - Chapter 4	
0	3-13	06 - Chapter 4		
	3-15	Qu chupter i		
9	3-18			
-	3-20	Spring Break		
	3-22	F B		Chapter 6
10	3-25		H08 - Chapter 5	
	3-27	Q7 - Chapter 4		
	3-29			Chapter 9
11	4-01		H09 - Chapter 6 (6.1 - 6.3)	
	4-03	Exam 2		
	4-05			
12	4-08		H10 – Chapter 9	
	4-10	Q8 - Chapter 5		
12	4-12			
13	4-15	00 Chantas 0	HII - Chapter 9	
	4-1/	Q9 – Chapter 9		
14	4-19		H12 Chapter 9	
14	4-22	010 Chapter 0	H12 - Chapter 9	
	4-24 ⊿_26	Q10 - Chapter 9		Chapter 10
15	4_20		H13 - Chapter 9 and Section 6 A	
1.5	5-01	011 – Chapter 9		
	5-03			
16	5-06		H14 - Chapter 10	
	5-08	O12 – Chapter 10	rr	
	5-10			
17		Final's Week		